## THE UNITED STATES PATENT AND TRADEMARK OFFICE

Scott A. Rosenberg § Group Art Unit:

2673

Anthony C. Miller S RECEIVED

AUG 0 8 2001

Serial No.: 09/365,363

§ § Examiner: N. Patel

Technology Center 2600

Filed:

For:

JULY 30, 1999

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LIOUID CRYSTAL OVER

SEMICONDUCTOR DISPLAY

WITH ON-CHIP STORAGE

§ Atty. Dkt. No.: INTL-0219-US

Commissioner for Patents Box AF Washington DC 20231

## REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed July 18, 2001, please amend the application as follows:

## In the Claims:

Please cancel claims 1-12 and 21-25.

## Remarks:

In order to simplify issues, claims have been canceled. Claim 13 calls for a refresh circuit coupled to a memory array and a pixel array, the refresh circuit adapted to refresh the memory array and the pixel array. Similarly, claim 17 calls for refreshing a memory array and a pixel array in the same refresh cycle.

It is asserted that Ouanrud teaches a refresh circuit coupled to the memory array and the pixel array and further adapted to refresh the memory array and the pixel array, citing

> Date of Deposit: I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, Washington DC 20231.

Lisa O'Sullivan

column 6, lines 18-28. However, these lines simply do not support any such features. There is no teaching of so connecting the memory array and the pixel array to be refreshed in the same refresh cycle. Therefore, the rejection is without any support and should be withdrawn.

In view of these remarks, the application is now in condition for allowance and the Examiner's prompt action in accordance therewith is respectfully requested. It is noted that the present amendment raises no new issues and should be entered as a matter of course.

Respectfully submitted,

Date:

Timothy N. Trop

Registration No. 28,994 TROP, PRUNER & HU, P.C. 8554 Katy Freeway, Suite 100

Houston, Texas 77024 (713) 468-8880 [Phone]

(713) 468-8883 [Fax]